

PATENT NUMBER

U.S. UTILITY Patent Application

M4 O.I.P.E. SCANNED ID4 ca TM	PATENT DATE
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APPLICATION NO. 09/808261	CONT/PRIOR	CLASS 438	SUBCLASS 199	ART UNIT 2812	EXAMINER Lattin
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Suraj Mathew
English Teacher

Method to Fabricate Low-Cost High-Performance Transistor CMOS

PTO-2040
12/89

ISSUING CLASSIFICATION								
ORIGINAL			CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg	Figs Drwg	Print Fig	Total Claims	Print Claim for O.G.
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<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	_____ (Primary Examiner) _____ (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input checked="" type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER	
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